



Atty. Docket No.: NLMI.P109

PATENT

IN THE UNITED STATES PATENT OFFICE

In Re Patent Application of:

First Named Inventor: Nataraj, Bindiganavale S.

Application No.: 10/000,122

Filed: 10/31/2001

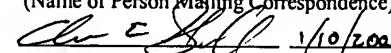
Title: CONTENT ADDRESSABLE MEMORY WITH
PROGRAMMABLE WORD WIDTH AND
PROGRAMMABLE PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Examiner: Elmore, Reba I.

Art Unit: 2187

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450 on January 10, 2005

Charles E. Shemwell
(Name of Person Mailing Correspondence)

Signature 1/10/2005
Date

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

Sir:

Enclosed is an Information Disclosure Citation Form 1449/PTO together with a copy of each reference cited therein, excluding U.S. Patents and Published U.S. Patent Applications. It is respectfully requested that the cited references be considered and that the enclosed copy of the Form 1449/PTO be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

This Information Disclosure Statement is being submitted pursuant to 37 CFR 1.97(c). A check to cover the \$180.00 fee under 1.17(p) is enclosed.

Pursuant to 37 CFR 1.97(h), the submission of this Information Disclosure Statement is not to be construed as an admission that the information cited in this statement is material to patentability.

The Commissioner is hereby authorized to charge any fees which may be required in connection with this submission to Deposit Account No. 501914.

Respectfully submitted,

SHEMWELL GREGORY & COURTNEY LLP

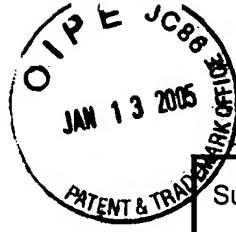

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01/18/2005 TLUU11 00000004 10000122

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Date January 10, 2005



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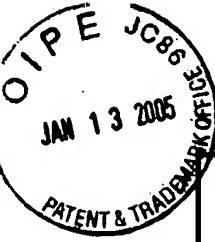
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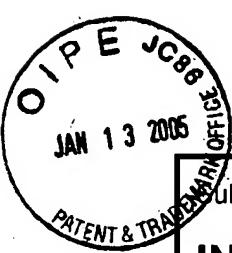
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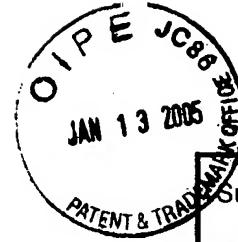
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	6289414		Feldmeier et al.	Oct., 1998	
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Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans-lation
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	Huang et al., "A Fast IP-Routing Lookup Scheme for Gigabit Switching Routers," IEEE, 1999, pp. 1429-1436	
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	Shin et al., "A Special-Purpose Content Addressable Memory Chip for Real-Time Image Processing," IEEE Journal of Solid-State Circuits, Vol. 27, No. 5, May 1992, pp. 737-744	

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